

5G 云网融合论坛

基于 Cloud Native 的 应用转型

马建伟

英特尔网络平台事业部

The Intel logo, consisting of the word "intel" in a lowercase, sans-serif font, followed by a registered trademark symbol (®).

intel®

免责声明

- Intel technologies' features and benefits depend on system configuration and may require enabled hardware, software or service activation. Performance varies depending on system configuration.
- No product or component can be absolutely secure.
- Tests document performance of components on a particular test, in specific systems. Differences in hardware, software, or configuration will affect actual performance. For more complete information about performance and benchmark results, visit <http://www.intel.com/benchmarks>.
- Software and workloads used in performance tests may have been optimized for performance only on Intel microprocessors. Performance tests, such as SYSmark and MobileMark, are measured using specific computer systems, components, software, operations and functions. Any change to any of those factors may cause the results to vary. You should consult other information and performance tests to assist you in fully evaluating your contemplated purchases, including the performance of that product when combined with other products. For more complete information visit <http://www.intel.com/benchmarks>.
- Intel Advanced Vector Extensions (Intel AVX) provides higher throughput to certain processor operations. Due to varying processor power characteristics, utilizing AVX instructions may cause a) some parts to operate at less than the rated frequency and b) some parts with Intel® Turbo Boost Technology 2.0 to not achieve any or maximum turbo frequencies. Performance varies depending on hardware, software, and system configuration and you can learn more at <http://www.intel.com/go/turbo>.
- Intel's compilers may or may not optimize to the same degree for non-Intel microprocessors for optimizations that are not unique to Intel microprocessors. These optimizations include SSE2, SSE3, and SSSE3 instruction sets and other optimizations. Intel does not guarantee the availability, functionality, or effectiveness of any optimization on microprocessors not manufactured by Intel. Microprocessor-dependent optimizations in this product are intended for use with Intel microprocessors. Certain optimizations not specific to Intel microarchitecture are reserved for Intel microprocessors. Please refer to the applicable product User and Reference Guides for more information regarding the specific instruction sets covered by this notice.
- Cost reduction scenarios described are intended as examples of how a given Intel-based product, in the specified circumstances and configurations, may affect future costs and provide cost savings. Circumstances will vary. Intel does not guarantee any costs or cost reduction.
- Intel does not control or audit third-party benchmark data or the web sites referenced in this document. You should visit the referenced web site and confirm whether referenced data are accurate.
- © Intel Corporation. Intel, the Intel logo, and other Intel marks are trademarks of Intel Corporation or its subsidiaries. Other names and brands may be claimed as the property of others.



目录

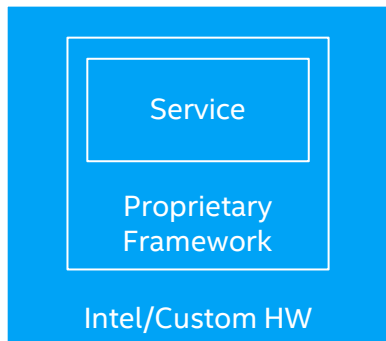
- Cloud Native 背景
- 基于 Cloud Native 的 5G 核心网架构
- 英特尔对 Cloud Native 的社区贡献
- 基于 Cloud Native 的参考设计架构

Cloud Native 演进之路

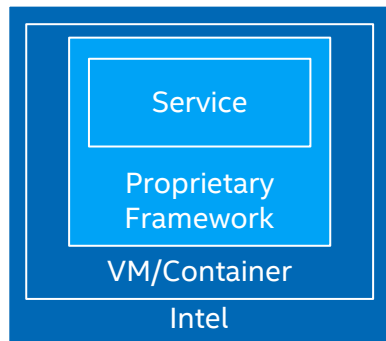
Vendor

Open

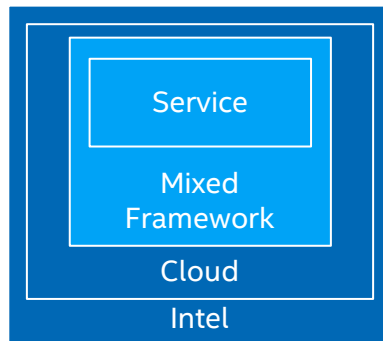
物理网元



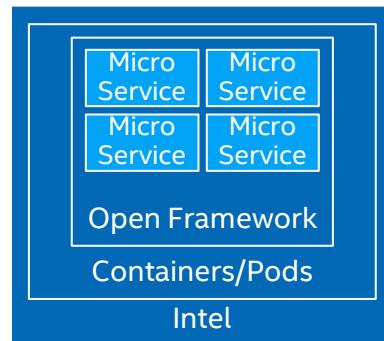
网络功能虚拟化



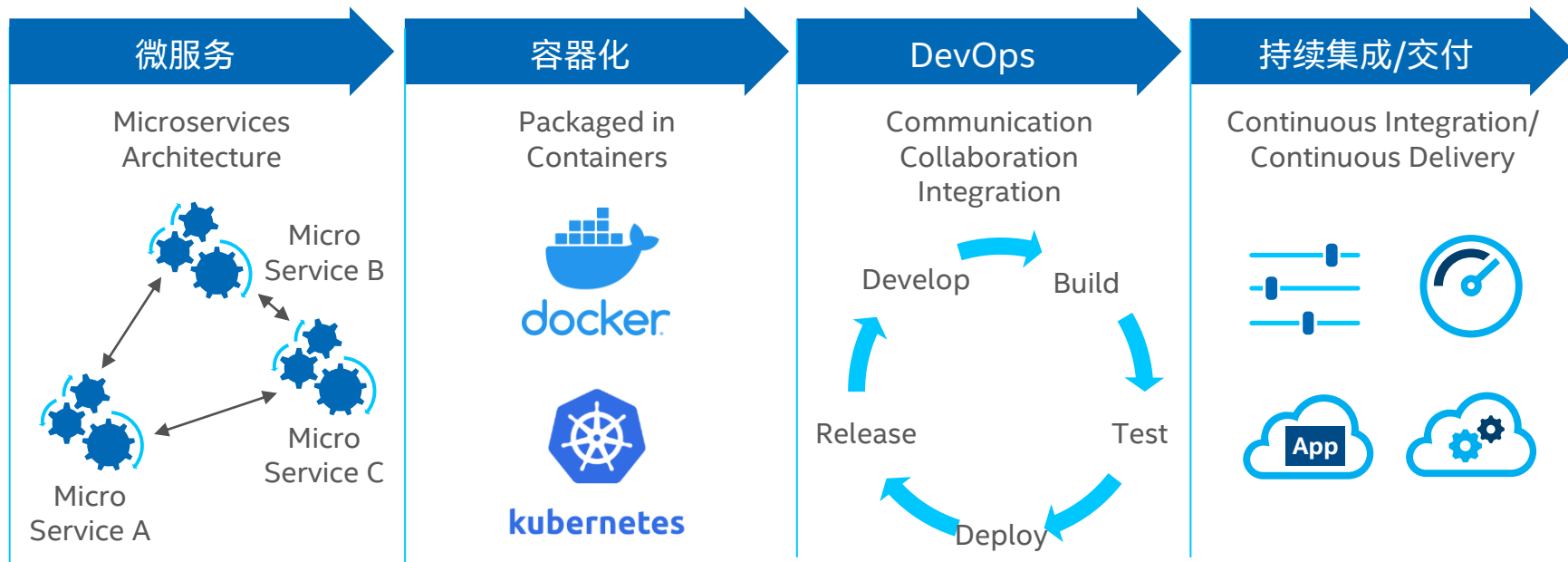
云化部署



云原生设计



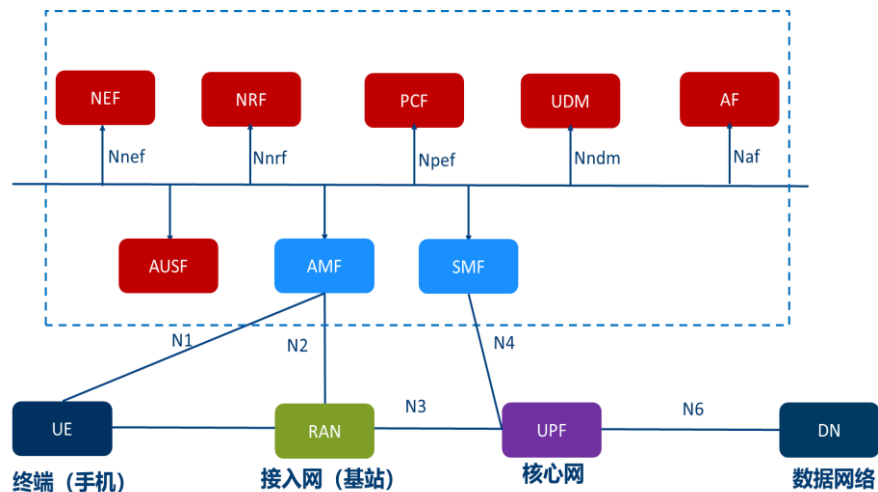
Cloud Native 架构和组成



快速的创新，集成，部署

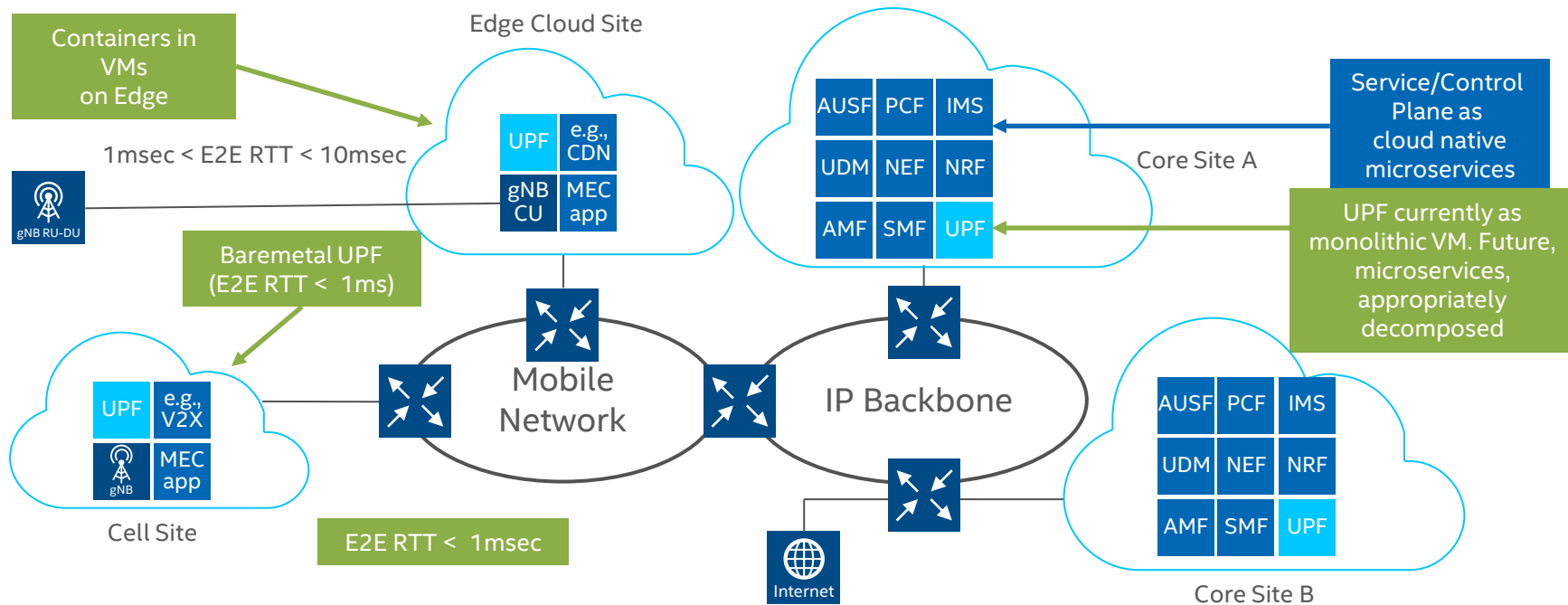
基于 Cloud Native 的 5G 核心网基础架构

- User equipment (UE)
- (Radio) Access Network (RAN)
- User Plane Function (UPF)
- Access and Mobility Function (AMF)
- Session Management Function (SMF)
- Policy Control Function (PCF)
- Authentication Server Function (AUSF)
- User Data Management (UDM)



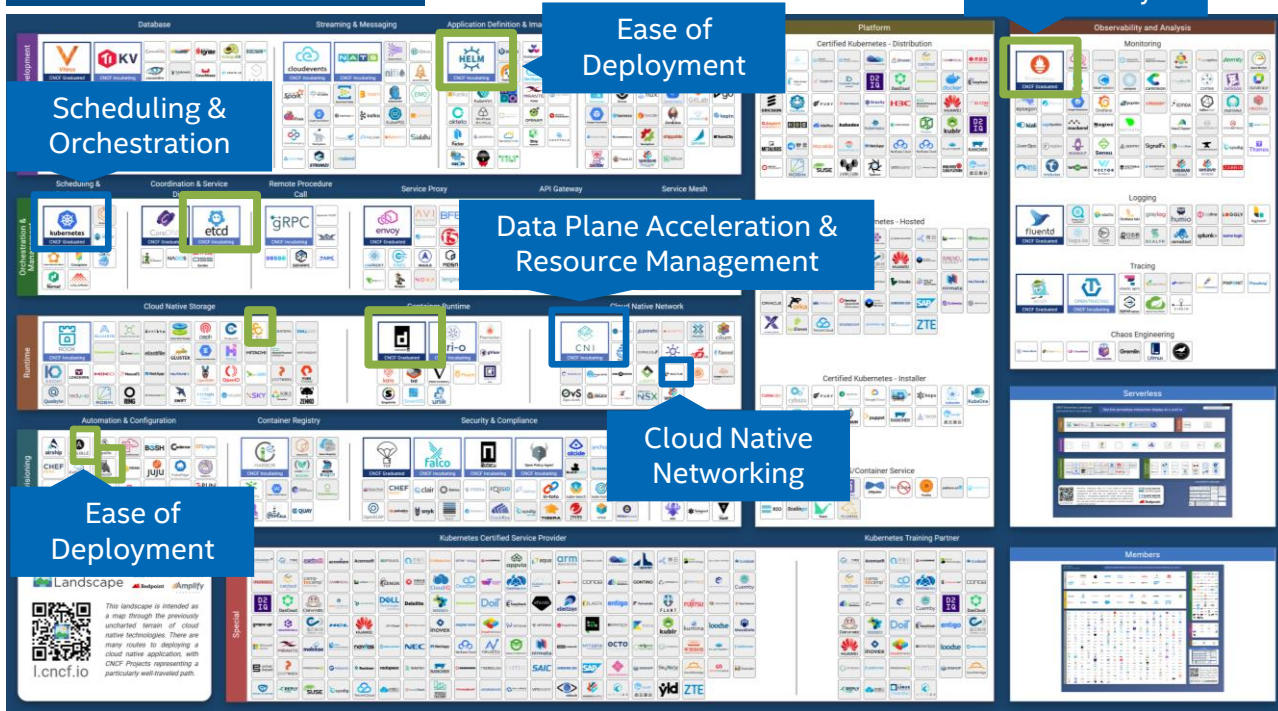
With 5G Core SA, cloud native is not nice to have but a must have !!

基于 5G 的端到端解决方案



UPF currently as monolith. Future as microservices?

英特尔与社区的合作



- Projects contributing to
- Tools used

Activities:

- CNCF Telecom User Group
- CNCF CNF Testbed

Kubernetes Special Interest Groups (SIGs) and Work Groups

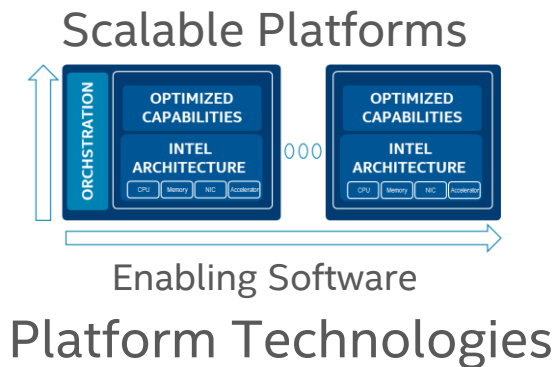
WG: Resource Management

SIGs: Network, Node, Scheduling & Instrumentation

Source: <https://github.com/cncf/landscape>
*Other names and brands may be claimed as the property of others.

Cloud Native 在电信领域的痛点

自动化部署
应用可移植
报文处理加速
资源管理
平台安全



Intel® Ethernet Controller Intel® QuickAssist Technology

Supporting material available on Intel® Network Builders site:



















Network Transformation Experience Kits: <https://networkbuilders.intel.com/network-technologies/network-transformation-exp-kits>

Containers Experience Kits: <https://networkbuilders.intel.com/network-technologies/container-experience-kits>

英特尔对 Cloud Native 的增强

Industry Gaps

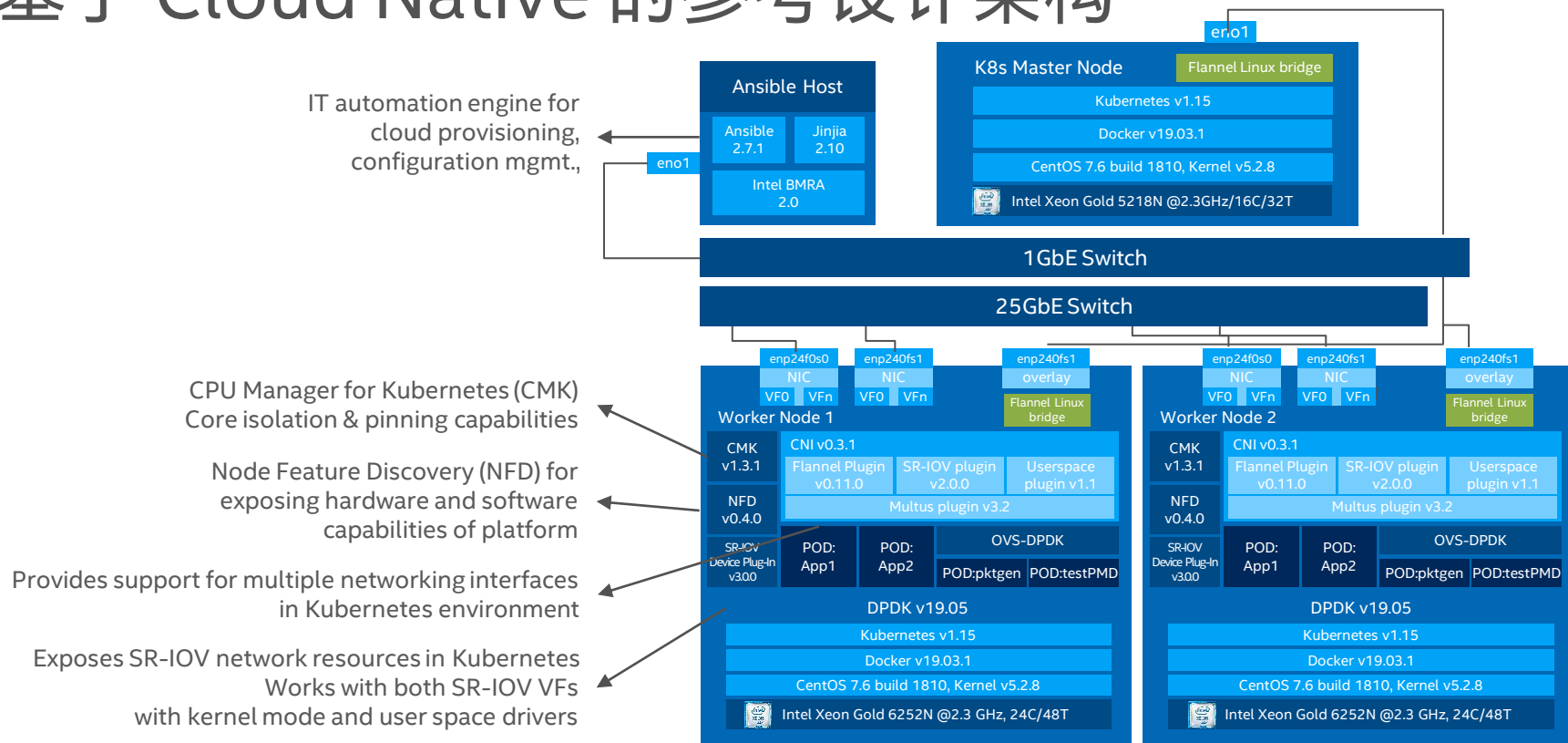
Addressed

K8s Networking 	Multiple Network for CNF	 MULTUS
Packet Processing 	High Performance E-W	 CNI  USERSPACE CNI  DPDK
	High Performance N-S	 CNI  SR-IOV  DPDK
	HA Networking	 CNI  BOND-CNI
Resource Management (Enhanced Platform Awareness) 	Platform Discovery	Node Feature Discovery (Intel AVX; SR-IOV; etc.)
	CPU Pinning/Isolation	CPU Manager for Kubernetes (CMK)
	Dynamic Huge Page	Native Huge Page Support for Kubernetes
	Manage Devices	Device Plugin (SR-IOV, Intel QAT, GPU, user space)
	Set NUMA Alignment	Topology Manager (NUMA)
Telemetry 	Scheduling per Telemetry	Telemetry Aware Scheduler
	Platform Telemetry	 collectd
Ease of Deployment 	Deployment Playbook	 HELM  ANSIBLE 

Experience Kits:

[https://networkbuilders.intel.com/
network-technologies/container-
experience-kits](https://networkbuilders.intel.com/network-technologies/container-experience-kits)

基于 Cloud Native 的参考设计架构



<https://github.com/intel/container-experience-kits/>

<https://builders.intel.com/docs/networkbuilders/container-bare-metal-for-2nd-generation-intel-xeon-scalable-processor.pdf>

